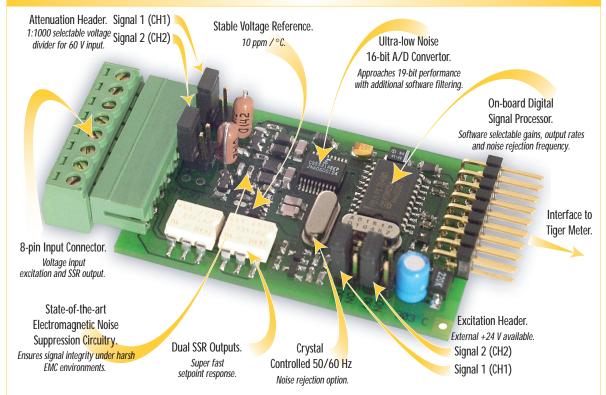
Fits Tiger 320 Series

16-BIT SMART DC VOLTS INPUT MODULE



The answer to accurate voltage measurements and switched relay outputs.

When faced with the task of supplying precise and stable voltage measurements over a large dynamic range, the automation engineer now has the solution at his fingertips.

Combined with the Tiger 320 Series operating system, this module is the smart design solution for many and varied control applications.

Input Module Order Code Suffix

ISD1 (50 Hz Rejection)

ISD2 (60 Hz Rejection)

ISD3 (50 Hz with SSRs)

ISD4 (60 Hz with SSRs)

Hardware Module Specifications							
Input Channels	1 of 2 inputs available and chosen through software.						
Input Range	Software selectable from \pm 25 mV to \pm 2 V for signal (1)						
	and fixed ± 1 V for signal (2), + 2.1 V common mode.						
Attenuation Header	1: 1000 voltage divider on both inputs for ≤ 60 V						
	with optional current shunt configuration.						
Excitation Header	+ 24 V (50 mA) available to power external sensors.						
Input Sensitivity	0.08 μV / count maximum.						
Zero Drift	± 40 nV / °C typical.						
Span Drift	± 5 ppm / °C of full scale maximum.						
Non-linearity	± 0.003% of full scale maximum.						
Input Noise	160 nVp-p typical at 1 Hz output rate.						
Signal processing Rate	50 Hz maximum, 1 Hz minimum.						
Solid Sate Relays (SSR)	17 Ω, 140 mA (± 400 V breakdown).						

Software Module Features						
Output Rates	A choice of average response outputs, 1-50 Hz.					
Gain Select	A choice of 7 voltage ranges from \pm 25 mV to \pm 2 V.					
Frequency Select	50 / 60 Hz noise rejection (Software selectable).					
Setpoint Switching	High speed (>1 ms) SSR outputs under setpoint control.					

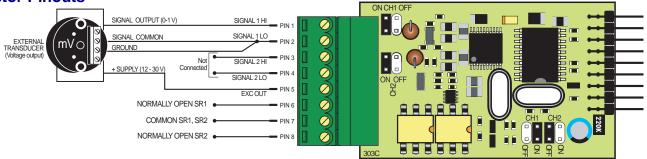
Some Relevant Tiger 320 Series Operating System Features				
	Setpoint Timer Functions.			
	Setpoint Register Reset and Trigger Functions.			
	On-demand Calibration.			
	Macro Compiler for PLC Functions.			
	32-Point Linearization.			
	Totalizer and Serial Printing.			

Amps DC from Shunt

Resistance

Programming Quick Start Guide

Connector Pinouts



The diagram shows an external transducer requiring external excitation wired to input module ISD3 or ISD4 through signal 1.

A signal <2 V requires the **signal 1 (CH1) attenuation header** to be set to the ON position.

The external supply requires **signal 1 (CH1) excitation header** to be set to the ON position.

Smart Setup Registers

The meter uses three smart setup registers to configure all smart input modules. Input modules ISD1 and ISD2 require **smart register 1** to be set up, while input modules ISD3 and ISD4 require **smart register 1** and **smart register 2** to be set up. All four modules are single input signal modules with the choice of two channels. ISD3 and ISD4 also has two solid state relay (SSR) outputs driven by SP5 and SP6 control. SSR1 is controlled by SP5 and SSR2 is controlled by SP6.

Programming Procedures

The following programming procedures cover all the steps required to configure ISD1 to ISD4. Steps 1 to 5 describe how to select the line frequency rejection, the voltage range and input signal, and the output rate through smart register 1.

Steps 6 to 9 describe how to select the SSR (SR1 and SR2) output mode for SP5 and SP6 control through **smart register 2**. Steps 10 onwards describe how to select the output register for channels 1, 2, 3, and 4 as required.

Note, as ISD1 and ISD2 do not have SSRs, \mathbf{smart} register $\mathbf{2}$ is \mathbf{not} programmed.



ISD1 is factory software set to 50 Hz rejection.

ISD2 is factory software set to 60 Hz rejection.

ISD3 is factory software set to 50 Hz rejection with two 140 mA SSRs.

ISD4 is factory software set to 60 Hz rejection with two 140 mA SSRs.

- Press the P and buttons at the same time to enter the main programming mode.
- Press the P button three times to enter Code 2. Set Code 2 to [X77].



lz | 0 Voltage, Current |
1 TC (3rd digit selects type of TC) |
2 RTD 3-wire (3rd digit selects type of RTD) |
3 RTD 2- or 4-wire (3rd digit selects type of RTD) |
4 Frequency |
5 Period

THIRD DIGIT

OUTPUT REGISTER MAP

0 Averaged signal 1 or 2
1 Rapid response signal 1 or 2*
2 Peak signal 1 or 2*
3 Valley signal 1 or 2*
4 Capture signal 1 or 2*
5 Rate of change of signal 1 or 2
7 Smart input module register 1 code setup

3 Press the P button.

This enters smart register 1 code setup menu.

The 1st digit setting is not relevant to this

step. 0 is the default setting.

- * Signal output at the A/D sampling rate.
- * Hardwire initiated from meter capture pin.

Counter



Note the output register map is different for each smart input module.

<u>5P7E | 000</u> →

2nd digit settings 0 to 6 allows you to select input signal 1 with a range of full scale voltage settings from -25 mV (setting 6) to -2 V (setting 0).

Setting 7 allows you to select input signal 2 with a $-1\ V$ full scale setting.

Both input signals can accept < 60 V utilizing the on-board 1:1000 attenuation header or even be configured for a current shunt.

FIRST DIGIT SECULD SECURD SECURD SECULD SECULD SECULD SECULD SECULD SECULD SECULD SECULD SECURD SECURD SECURD SECU

1 60 Hz rejection (ISD2/ISD4 default setting)

3 50 Hz rejection (ISD1/ISD3 default setting)

SECOND DIGIT FULL SCALE SIGNAL

SECOND DIGIT

MEASUREMENT TASK

Signal 1: \pm 2.0 V

Signal 1: ± 1 V
 Signal 1: ± 500 mV

3 Signal 1: \pm 250 mV

 $\begin{array}{ll} \text{4} & \text{Signal 1:} \pm \ 100 \ \text{mV} \\ \\ \text{5} & \text{Signal 1:} \pm \ 50 \ \text{mV} \end{array}$

6 Signal 1: ± 25 mV7 Signal 2: ± 1 V

OUTPUT RATE

0 1 Hz averaged: 50/60 Hz rapid response

THIRD DIGIT

- 10 Hz averaged: 50/60 Hz rapid response
 10 Hz averaged: 800/960 Hz rapid response
- 3 50/60 Hz averaged: 800/960 Hz rapid response
- 4 50/60 Hz averaged: 400/480 Hz rapid
- 5 50/60 Hz averaged: 200/240 Hz rapid response
- 6 -

- 4 Using the buttons, select the relevant line frequency rejection, input signal and range, and the output rate. settings.
- Press the P button.

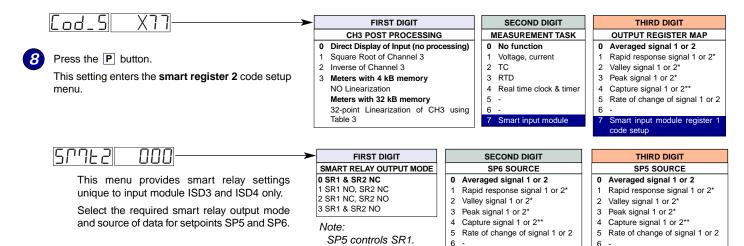
This takes you back to the Code 2 menu.



Using the ▶ button, reset the 3rd digit to zero [X70] to leave the smart register 1 menu. Note, leaving the 3rd digit as 7 means the display constantly cycles between [Cod_2] and [SMt1].



Press the P three times to enter Code 5. Set Code 5 to [X77].



SP6 controls SR2.

7

Note:

- Press the P button to save the settings. The display toggles between [Cod_5] and [X77].
- Using the ♠ ▶ buttons, reset the 3rd digit to 0 to leave the smart register 2 menu.
- Press the P and 1 buttons at the same time to return to the operational display.

Select a Channel

Select the output register for the required channels

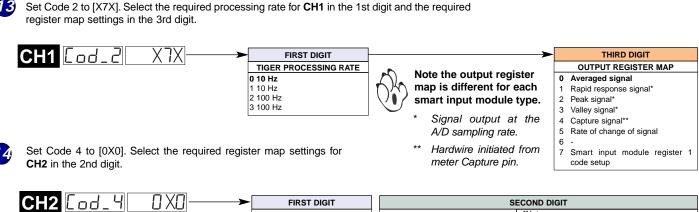
Press the P and 1 button at the same time again to re-enter the main programming mode, then press the P button three times to enter Code 2.

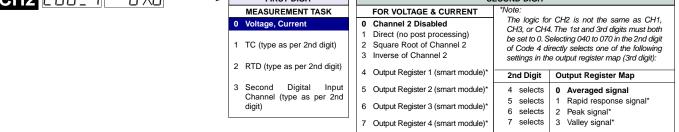
Signal output at the A/D sampling rate.

Hardwire initiated from meter Capture pin.

Reset of Peak, Valley, and Capture Signals Reset of peak/valley/capture signals options are:

- 1. If peak/valley/capture signals are stored in CH1, CH2, CH3, or CH4, a macro can reset CH1 by resetting register 253, CH2 by resetting register 252, CH3 by resetting register 251, and CH4 by resetting register 250.
- 2. As for Step 1, but using the LOCK pin to reset.
- 3. As for Step 1, but only applying to CH1 using the HOLD pin to reset.
- 4. As for Step 1, but using SPC1 to reset CH1, SPC3 to reset CH3, and SPC4 to reset CH4.
- Set Code 2 to [X7X]. Select the required processing rate for CH1 in the 1st digit and the required register map settings in the 3rd digit.





All linearization tables are set up in the Calibration Mode [24X].

If required enter Code 5 and select the required register map settings for CH3 in the 3rd digit.

Customer Configuration Settings:

	1st Digit	2nd Digit	3rd Digit		1st Digit	2nd Digit	3rd Digit
SPAF 1				CH1 [0d_2		7	
					4 . 5: "		0 - 1 Di - 1
	1st Digit	2nd Digit	3rd Digit		•	2nd Digit	3ra Digit
SLUF5				CH2 [0d_4			0
					1st Diait	2nd Digit	3rd Digit
				CH3 [-d _ 5		7	
					1st Digit	2nd Digit	3rd Digit
				CH4 [od_5		7	

Table 4

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