

An extremely flexible and powerful dual input mV/V Input Module

This input module comes with a myriad of hardware and software options to satisfy the requirements of precision voltage measurement and dual input functionality. When combined with the Tiger 320 Series operating system, the OEM has a powerful solution to applications ranging from small voltage measurement through to process control, all at a very affordable price.

Input Module **Order Code Suffix**

ISDA (50 Hz Rejection)

ISDB (60 Hz Rejection)



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	Hardware Module Specifications	
nput Range	Software selectable from 25 mV to 2 V, +2.1 V common mode.	
	Maximum 60 V using signal attenuation header.	
nput Channels	Dual with independent gains. Zero X-talk between channels	
	each having 19-bit effective resolution.	IN
nput Sensitivity	0.08 μV/Count maximum.	
Zero Drift	± 40 nV/ °C typical.	Dual Precis
Span Drift	± 5 ppm/ °C of full scale maximum.	DC V
Non-linearity	± 0.003 % of full scale maximum.	
nput noise	160 nVp-p typical at 1 Hz output rate.	
Signal Processing Rate	20 Hz maximum, 1 Hz minimum.	
Excitation Voltage	+24 V (50mA) available to power external sensors.	
J. J		Volts I
	Software Module Features	
Output Rates	Choice of 4 average response outputs, 1-20 Hz.	Amps fro
Gain Select	Choice of 7 voltage ranges from \pm 25 mV to \pm 2 V.	DC Shi
Frequency Select	50/60 Hz noise rejection (Software selectable).	
Some Releva	ant Tiger 320 Series Operating System Features	
	Auto Zero Maintenance.	
	Set TARE, Reset TARE.	
	Setpoint Timer Functions.	
	Setpoint Register Reset and Trigger Functions.	
	On-demand Calibration.	
	Macro Compiler for PLC Functions.	
	32-Point Linearization.	
	Totalizer and Serial Printing.	

Programming Quick Start Guide

Connector Pinouts



Smart Setup Registers

The meter has three smart setup registers to configure all smart input modules.

ISDA and ISDB require smart registers 1 and 2 to be configured.Because this is a dual input module independent sensor inputs can be software selected for Tiger 320 Series meter channels 1, 2, 3, and 4. This module produces two output registers. One of these registers can be transferred to Channel 1 via Code 2, the same or another register to Channel 2 via Code 4, the same or another register to Channel 3 via Code 5, and t he same or another register to Channel 4 via Code 6.



Programming Procedures

The following programming procedures cover all the steps required to configure smart input module ISDA and ISDB. Steps 1 to 5 describe how to select the **input signal 1** voltage range, line frequency rejection, and the output rate through **smart setup register 1**.

Steps 6 to 9 describe how to select input signal 2 voltage range through smart setup register 2.

Steps 10 to 19 describe how to select the output register for channels 1, 2, 3, or 4 as required.

Press the P and 🛧 buttons at the same time to enter the main programming mode.

Press the P button twice to enter Code 2 for input signal 1 configuration settings. Set Code 2 to [X77].

[od_2] X1] →	FIRST DIGIT	SECOND DIGIT	THIRD DIGIT
This setting enters the smart register 1 code setup menu.	0 10 Hz 1 10 Hz 2 100 Hz 3 100 Hz	 Voltage, Current 1 TC (3rd digit selects type of TC) 2 RTD 3-wire (3rd digit selects type of RTD) 	Averaged Signal 1 Averaged Signal 2 - 3 -
Note the output register map is different for each smart input module.		 RTD 2- or 4-wire (3rd digit selects type of RTD) Frequency Period Counter Smart Input Module 	4 - 5 - 6 - 7 Smart input module register 1 code setup

3 Press the P button.		This men u p	ro vides settin	as unique to smart	
This enters smart register f	I code setup menu.	register 1 of	the ISDA and	ISDB input module.	
		FIRST [DIGIT	SECOND DIGIT	THIRD DIGIT
		REFERENCE	VOLTAGE	SIGNAL 1 VOLTAGE RAN	GE OUTPUT RATE
Note:		1 60 Hz rejection		1 ±1 V	1 5 Hz averaged
When the input signal is greater than 2 V the 1:100 attenua-			etting)	2 ±500 mV	2 10 Hz averaged
For signals up to 10V select the ±	3 50Hz rejection	otting)	3 ±250 mV 4 ±100 mV	4 - 5 - 6 -	
olution.		etting)	5 ±50 mV		
For signals larger than 10 V selec	t±1 V range.			6 ±25 mV 7 -	7 -
4 Using the	ect the relevant line frequ gnal 1, and the output ra	iency rejec- ite common			
5 Press the P button. The dis	splay returns to [Cod_2] [/	x77]. [od	_2 X		
▲ Using the ↓ button reset t	the 3rd digit to zero [X70]	to leave the sm	art register 1	menu	חרא
Note leaving the 3rd digit a	r = 7 means the display co		hatwoon [Cod	2] and [SMt1]	
Note, leaving the Studigit as	s / means the display co			i_zj anu [Sivit i].	
-					
7 Press the P button three t	imes to enter Code 5 for	input signal 2	configuration	settings. Set Code 5 to [λ	(77].
[od 5] X]]→	► FIR:	ST DIGIT		SECOND DIGIT	THIRD DIGIT
j_ · · · · ·	CH3 POST			MEASUREMENT TASK	OUTPUT REGISTER MAP
Note the output regis-	1 Square Root of Channel 3	processing)		1 Voltage, current	1 Averaged Signal 2
ters in the 3rd digit are	2 Inverse of Channel 3 3 Meters with 4 kB memory	,		2 TC 2 PTD	2 -
ISDB These register s	NO Linearization Meters with 32 kB memor	v		4 Real time clock & timer	4 -
vary for each different	32-point Linearization of CH	H3 using Table 3		5 -	5 -
smart input module.	Note: All linearization tables are se	et up in the Calibation	n Mode [24X].	6 - 7 Smart input module	o -7 Smart input module register 2 code setup
8 Press the P button. This setting enters the sma	rt register 2 code setup r	menu.			
<u>51''122 UUU</u> -		FIRST	DIGIT	SECOND DIGIT	THIRD DIGIT
		Not Re	levalit	Not Relevant	0 ±2 V
y Using the ⊥ buttons, so	elect the sensor 2				1 ±1 V 2 ±500 mV
input nom the ord digit.					3 ±250 mV
	in the eattings				4 ±100 mV 5 ±50 mV
The displayer for the hot way					6 ±25 mV
The display loggles betwee		100-5	<u>X i i</u>		7 -
1 Using the ■ button, reset t	he 3rd digit to 0 to lea ve	the smart regist	ter 2 menu.		
Press the ℙ and ♠ butto	ons at the same time to re	tur n to the oper	ational displa	у.	
Select a Channel	Select the output rec	gister for the re	quired chann	els	
13 Press the ℙ and ▲ buttor	ו at the same time again t	to re-enter the m	nain prog ram	ming mode.	
14 Press the P button three ti	mes to enter Code 2.				
Set Code 2 to [X7X]. Select t register map setting in the 3r	he required processing ra d digit.	ate for CH1 in th	e 1st digit and	I the required	map is different for each smart input module type
		T DIGIT		SECOND DIGIT	THIRD DIGIT
		CESSING RATE	MEA	ASUREMENT TASK	OUTPUT REGISTER MAP
	0 10 HZ 1 10 Hz		1 TC (3rd digit	selects type of TC)	1 Averaged Signal 1
	2 100 Hz 3 100Hz		2 RTD 3-wire (3 RTD 2- or 4-v	3rd digit selects type of R TD) vire (3rd digit selects type of RTD)	2 - 3 -
			4 Frequency 5 Period	,	4 - 5 -
			6 Counter	Module	6 -
			r Smart input i	woulle	, Smart input module register 1 code setu



If required enter Code 4 and select the required register map settings for CH2 in the 2nd digit. Note, the 1st digit must be set to 0.



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