

Now available in Texmate's family of intelligent load cell input modules is the ISSC 50 Hz and the ISSD 60 Hz input module. With additional dual status inputs, standard weighing tasks can now utilize external control signals. When combined with the Tiger 320 Series controller, a versatile and interactive control system is now possible. For example, a continuous belt weigher with an encoder speed sensor.

#### Input Module Order Code Suffix

ISSC (50 Hz Rejection)

ISSD (60 Hz Rejection)



	Indiawale module specifications
Dual Status Inputs	•
D1 and D2	Configured as frequency inputs
	on CH1 & CH2 respectively.
Excitation Voltage	+24 V to power external sensors.
· · · ·	Voltage transitions 6 to 24 V.
Inputs	Open-collector configuration available
	(10 K pull-up resistors.)
Load Cell	On CH3 / CH4
Excitation	+5 V DC, 130 mA maximum.
Input Range	Software selectable 1 mV/V to 20 mV/V.
Input Sensitivity	0.08 μV/count maximum.
Zero Drift	± 40 nV/°C of full scale maximum.
Span Drift	± 5 ppm/°C of full scale maximum.
Non-linearity	± 0.003% of full scale maximum.
Input Noise	160 nV pp typical at 1 Hz output rate.
Signal Processing Rate	50 Hz maximum, 1 Hz minimum.
	Software Module Features
Dual Status Inputs	
Frequency Ranges	D1 (CH1), choice of 7 ranges. (0.01 Hz to 655.35 kHz).
	D2 (CH2), choice of 4 ranges. (0.01 Hz to 500 kHz).
Load cell	
Dual Output Rates	Rapid and average response outputs.
Peak & Valley Outputs	Monitoring over and undershoots.
Capture Output	Hardwire signal capture.
Rate of Change Output	Useful for fine tuning reaction times.
Line Frequency Rejection	50/60 Hz selectable.
Some Releva	ant Tiger 320 Series Operating System Features
	Auto-zero maintenance.
	Set TARE, reset TARE.
	Setpoint timer functions.
	Setpoint register reset and trigger functions.

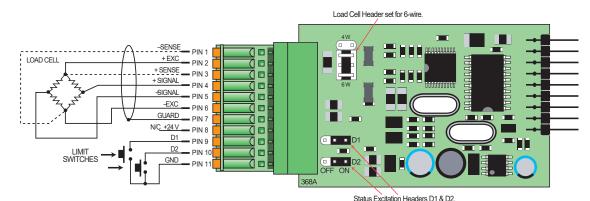
**Hardware Module Specifications** 

On-demand calibration.

LOAD-CELL PRESSURE

#### Programming Quick Start Guide

#### **Connector Pinouts**



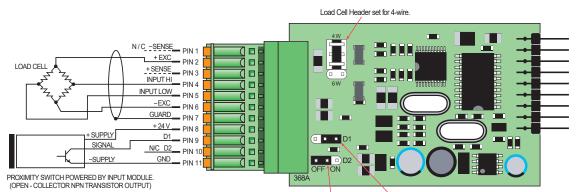
ISSC / ISSD input module showing 6-wire load cell configuration and two status inputs connected to limit switches.

#### Note:

Both status excitation headers are in the ON position to provide voltage transition from +24 V to 0 V on switch closure. Load cell header selected for 6-wire load cell.

Table 1	: ISSC / ISSD	11-pin I/O Connector
Pin	Description	Function
1	–Sense	Bridge – sense volts
2	+EXC	Bridge + volts excitation
3	+Sense	Bridge + sense volts
4	+Signal	Bridge + signal output
5	–Signal	Bridge – signal output
6	–EXC	Bridge – volt excitation
7	Shield	Cable shield (+2.5 volt)
8	+24 V	Not connected
9	D1	Status input D1 (CH1)
10	D2	Status input D2 (CH2)
11	GND	Common ground

#### Figure 1 – ISSC / ISSD Configured for 6-wire Load Cell and Two Status Inputs



Status Excitation Headers D1 & D2.

ISSC / ISSD input module showing 4-wire load cell configuration and one status input connected to a pro ximity switch.

Status input D1 is the signal from an open collector NPN transistor output in a proximity detector. Status excitation header D2 must be in the ON position f or the transistor to provide +24 V to GND voltage transitions.

D2 is not connected.

Table 1	1: ISSC / ISSD	11-pin I/O Connector
Pin	Description	Function
1	-Sense	Not connected
2	+EXC	Bridge + volts excitation
3	+Sense	Bridge + sense volts
4	Input High	Bridge + signal output
5	Input Low	Bridge – signal output
6	-EXC	Bridge – volt excitation
7	Shield	Cable shield (+2.5 volt)
8	+ 24 V	+ 24 V excitation
9	D1	Status input D1 (CH1)
10	D2	Not connected
11	GND	Common ground

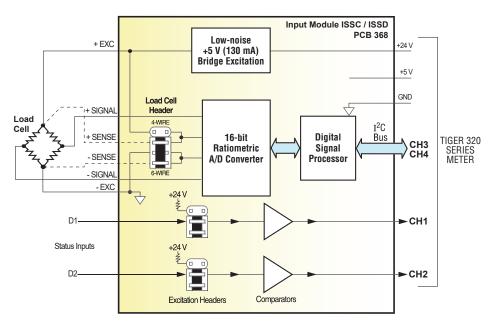
#### Figure 2 – ISSC / ISSD Configured for 4-wire Load Cell and One Status Input

### **Technical Description**

The load cell input signal is processed in the input module's 16-bit A/D converter and digital signal processor from where it can be fed to either channel 3 (CH3) or channel 4 (CH4) or both. Status input D1 is internally assigned to channel 1 (CH1) and status input D2 is inter nally assigned to channel 2 (CH2).

CH1 is configured f or frequency measurement f or D1 in Code 2. CH2 is configured f or frequency measurement for D2 in Code 4. Selecting the load cell output f or CH3 is configured in Code 5 and f or CH4 in Code 6.

The line frequency rejection setting, sensor input range in mV/V, and the output rate are all selected in the smart register 1 (SMT1) menu in Code 2.





### **Smart Setup Registers**

The Tiger meter uses three smar t setup registers to configure all smar t input modules . ISSC / ISSD requires only **smart register 1** (SMT1) to be configured. See Figure 4.

SMT1 configures the load cell input signal for line frequency rejection, input signal range in mV/V, and output rate. SMT1 produces the following six output registers from the load cell input:

- Averaged signal.
- · Rapid response signal\*.
- · Peak signal\*.
- Valley signal\*.
- Capture signal\*\*.
- Rate-of-change signal.

Note:

- \* Signal output at the A/D sampling rate.
- \*\* Hardware initiated from meter CAPTURE pin.

One of these registers can be transferred to Channel 3 (CH3) via Code 2, the same or another register to CH4 via Code 6.

Load Cell Input Signal	CODE 2 Enter Code 2. Select smart register 1 setup [X77].	SMART REGISTER 1 SETUP           SMT1 allows you to enter the smart register 1 setup and select for the load cell sensor:           • Line Frequency.           • Sensor Input.           • Output Rate.		OUTPUT REGISTER MAP The output register map allo ws y ou to choose a selection of processed output sig- nals from the load cell input to either CH1 or CH2.	— СН3 — СН4
		SMART REGISTER 2 SETUP         Not required.         SMART REGISTER 3 SETUP         Not required.	] ]		

Figure 4 – ISSC / ISSD Smart Setup Register Operational Flow Diagram

The following programming procedures cover all the steps required to configure smar t input module ISSC/ISSD:

Steps 1 to 7:	Code 2 – SMT1 –	1) Line frequency rejection.
		2) Signal range in mV/V.
		3) Output rate.
Steps 8 to 9:	Code 2 – CH1 –	Frequency range for status input D1.
Steps 10 to 12:	Code 4 – CH2 –	Frequency range for status input D2.
Steps 13 to 14:	Code 5 – CH3 –	Output register for load cell input.
Steps 15 to 16:	Code 6 – CH4 –	Output register for load cell input.

## SMT1 Setup

Enter Code 2 and then enter SMT1 to configure line frequency rejection, signal r ange, and output rate settings.

Image: Second Dist       Second Dist         This setting enters the smart register 1 code       10 thz         10 thz       10 thz         2 100 Hz       10 thz         3 10 Hz       10 thz         10 thz       10 thz		FIRST DIGIT	SECOND DIGIT		THIRD DIGIT
This setting enters the smart register 1 code setup menu.       0 10 Hz 2 100 Hz 2 100 Hz 3 100 Hz       0 0 Votage Current 1 TC (ard digt selects type of TC) 2 RTD Surver (3rd digt selects type of RTD)       0 Votage Current 1 C (ard digt selects type of RTD)       1 R aging Response Signalt 2 Peak Signalt 3 Value Signalt 3 Value Signalt 4 Erequency 5 Period 6 Counter       1 R aging Response Signalt 2 Peak Signalt 3 Value Signalt 4 Erequency 5 Period 6 Counter       1 R aging Response Signalt 7 Smart Input Module         Image Signalt 5 Response Signalt 6 Counter       2 Smart Input Module       1 Response Signalt 7 Smart Input module register 0 Smart Input Module         Image Signalt 7 Smart Input Module       1 Response Signalt 7 Smart Input module.       1 Response Signalt 7 Smart Input module register 7 Smart Input module         Image Signalt 7 Smart Input module       1 Response Signalt 7 Smart Input module.       1 Response Signalt 8 Counter       1 Response Signalt 8 Counter         Image Signalt 7 Smart Input module       1 Response Signalt 8 Counter       1 Response Signalt 8 Counter       1 Response Signalt 8 Counter         Image Signalt 8 Response Signalt 1 Response 1 Response 1 Response 1 Response 1 Response Signalt 1 Response Si					
Signal output at the A/D sampling rate.     Signal output at the A/D sampling rate.     "     Signal output at the A/D sampling rate.     "     Hardware initiated from meter CAPTURE pile     "     Hardware initiated from meter CAPTURE     "     Interpreted from me		1 10 Hz 2 100 Hz	1 TC (3rd digit selects typ 2 RTD 3-wire (3rd digit se of RTD) 3 RTD 2- or 4-wire (3rd dig type of RTD) 4 Frequency 5 Period 6 Counter	elects type	<ol> <li>Rapid Response Signal*</li> <li>Peak Signal*</li> <li>Valley Signal*</li> <li>Capture Signal***</li> <li>Rate of Change Signal</li> <li>-</li> <li>Smart input module register</li> </ol>
LINE FREQUENCY SELECT       SENSOR INPUT mV/V       OUTPUT RATE         This menu provides settings unique to smart register 1 of input module ISSC/ISSD.       1.4 rejection       1       1       2       m/V       1       1 Hz average, 50/60 Hz rapid response       2       2       m/V       2       1       1 Hz average, 50/60 Hz rapid response       2       2       3       20 m/V       1       1 Hz average, 50/60 Hz rapid response       2       -       3       50/60 Hz rapid response       2       - </th <th>are ule. sma</th> <th>specific to the ISSC / IS These registers vary for</th> <th>SD input mod-</th> <th>Signal outpu</th> <th></th>	are ule. sma	specific to the ISSC / IS These registers vary for	SD input mod-	Signal outpu	
LINE FREQUENCY SELECT       SENSOR INPUT mV/V       OUTPUT RATE         This menu provides settings unique to smart register 1 of input module ISSC/ISSD.       0 60 Hz rejection       1       2 mV/V       1 1Hz average, 50/60 Hz rapid response       2       1 0 Hz average, 50/60 Hz rapid response       2       2       3 20 mV/V       1 0 Hz average, 50/60 Hz rapid response       2       -       3 50/60 Hz rapid response       2       -       -       -       -       -       -       -       - <th></th> <th>FIRST DIGIT</th> <th>SECOND DIGIT</th> <th></th> <th>THIRD DIGIT</th>		FIRST DIGIT	SECOND DIGIT		THIRD DIGIT
to smart register 1 of input module ISSC/ISSD. 1 2 mV/V 2 3 mV/V 3 20 mV/V 4 - 5 - 6 - 7 - Using the 1 10 Hz average, 50/60 Hz rapid response 2 - 3 50/60 Hz average, 800/960 Hz rapid response 4 - 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 4 - 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 4 - 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 4 - 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 4 - 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 4 - 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 5 - 6 - 7 - Using the 10 Hz average, 800/960 Hz rapid response 5 - 6 - 7 - 10 Hz line frequency rejection for 50 Hz power supply areas, or 60 Hz line frequency rejection for 60 Hz power supply areas. 2nd Digit: The sensor input range. 3rd Digit: The output rate.	LIN				
ISSC/ISSD. 3. 3. 3. 3. 3. 3. 3. 3. 3. 3		-			
4       -       5       -       5       -       5       -       6       -       7       -       6       -       7       -       -       7       -       -       -       7       -		Hz rejection			Hz average, 800/960 Hz rapid respon
6       -       7       -       7       -         Using the  Image: Using the Image frequency rejection for 50 Hz power supply areas, or 60 Hz line frequency rejection for 60 Hz power supply areas.       6       -       7       -       7       -       7       -         Using the Image frequency rejection for 50 Hz power supply areas, or 60 Hz line frequency rejection for 60 Hz power supply areas.       6       -       7       -       7       -       7       -       7       -       7       -       7       -       7       -       7       -       7       -       10 </td <td></td> <td></td> <td>3 20 mV/V</td> <td>3 50/60</td> <td></td>			3 20 mV/V	3 50/60	
<ul> <li>Using the</li></ul>			4 -	4 -	
<ul> <li>1st Digit: 50 Hz line frequency rejection for 50 Hz power supply areas, or 60 Hz line frequency rejection for 60 Hz power supply areas.</li> <li>2nd Digit: The sensor input range.</li> <li>3rd Digit: The output rate.</li> </ul>			4 - 5 - 6 -	4 - 5 - 6 -	
rejection for 60 Hz power supply areas. 2nd Digit: The sensor input range. 3rd Digit: The output rate.			4 - 5 - 6 -	4 - 5 - 6 -	
3rd Digit: The output rate.	2 50 ISSC/ISSD.		4 - 5 - 6 -	4 - 5 - 6 -	
	Using the 1 buttons, select: 1st Digit: 50 Hz line frequency rejection for 50	<b>Hz</b> power supply areas,	4 - 5 - 6 - 7 -	4 - 5 - 6 -	
Press the P button. The display returns to [Cod_2] [X77].	Using the ■ ■ buttons, select: 1st Digit: 50 Hz line frequency rejection for 50 rejection for 60 Hz power supply area	<b>Hz</b> power supply areas,	4 - 5 - 6 - 7 -	4 - 5 - 6 -	
	Using the ●● buttons, select:         1st Digit:       50 Hz line frequency rejection for 50 rejection for 60 Hz power supply area         2nd Digit:       The sensor input range.	<b>Hz</b> power supply areas,	4 - 5 - 6 - 7 -	4 - 5 - 6 -	

Configure the required channel for each input signal type

Status Input D1:Set up CH1 in Code 2.Status Input D2:Set up CH2 in Code 4.Load Cell Input:Set up CH3 in Code 5 or CH4 in Code 6.

## Status Input D1

Enter Code 2 and select the frequency range for CH1.

8 Press the P and the buttons at the same time again to re-enter the main programming mode, then press the P button three times to enter Code 2.

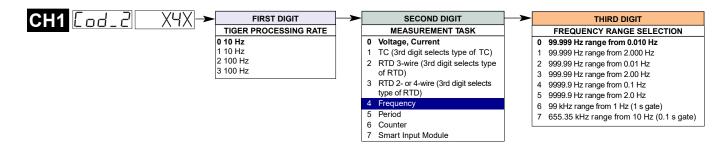




Set Code 2 to [X4X] to configure status input D1 on CH1.

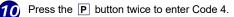
**1st Digit**: Not relevant digital input D1.

2nd Digit: Select 4 to enter the frequency range menu for digital input D1.3rd Digit: Select the required frequency range setting for digital input D1.



# Status Input D2

Enter Code 4 and select the digital input for CH2.



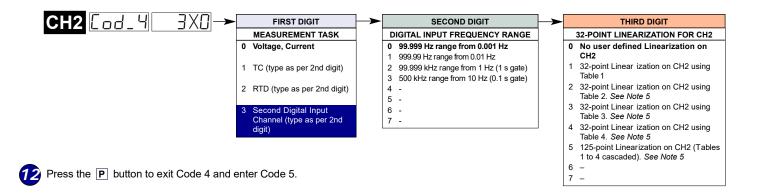
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Set Code 4 to [3X0] to configure status input D2 on CH2.

1st Digit: Select 3 to enter the second digital input menu.

2nd Digit: Select the frequency range for digital input D2.

3rd Digit: Select 0 to apply No user defined linearization on CH2.



# Load Cell Input

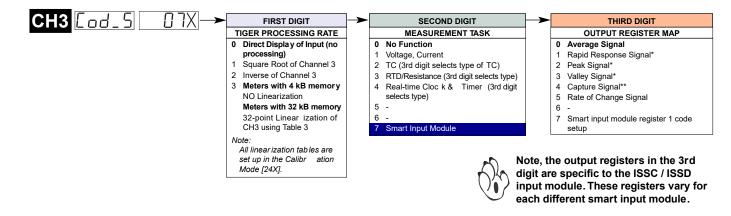
The load cell input can be selected for CH3 via Code 5 or CH4 via Code 6.

13 Set Code 5 to [07X] and select the required load cell output register f or CH3 in the 3rd digit.

1st Digit: Set to 0 to select Direct Display of Input.

2nd Digit: Select 7 to enter the output register map for the load cell signal.

3rd Digit: Select the required output register setting for the load cell signal.





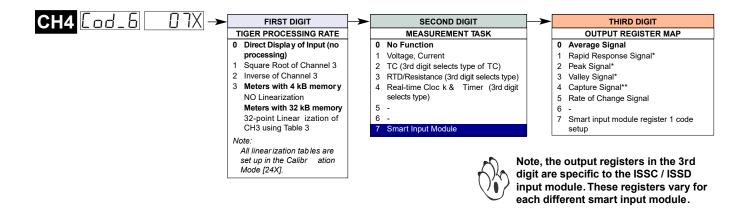
Press the P button to exit Code 5 and enter Code 6.

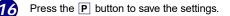
If a load cell output is required on CH4 set Code 6 to [07X] and select the required load cell output register for CH4 in the 3rd digit.

1st Digit: Set to 0 to select Direct Display of Input.

2nd Digit: Select 7 to enter the output register map for the load cell signal.

3rd Digit: Select the required output register setting for the load cell signal.





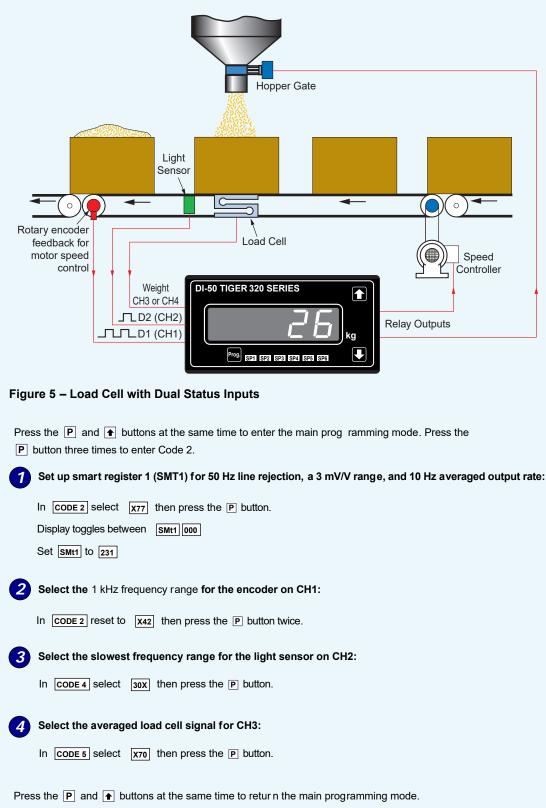
Press the ℙ and ♠ buttons at the same time to retur n to the operational display.

## Example Setup Procedure

In our example, a hopper fills boxes travelling along a conveyor belt. A rotary encoder monitors the conveyor speed and a light beam sensor indicates when the box to be filled is in position below the hopper. A load cell under the conveyor weighs the box.

The rotary encoder is connected to status input D1 and assigned to CH1. The light beam sensor is connected to status input D2 and assigned to CH2. The load cell is configured for 3 mV/V input with 50 Hz line frequency rejection. As the boxes are filled relatively quickly, the averaged output rate is set to 10 Hz on CH3.

The frequency output from the encoder is in the 0 to 1 kHz r ange (360 pulse/revolution). The light sensor has its beam broken at a much slower rate.



## **Customer Configuration Settings:**

	1st Digit	2nd Digit	3rd Digit
500	1E I		
		2nd Digit	3rd Digit
CH1 [ 00			
	1st Digit	2nd Digit	3rd Digit
CH2	_   3		0
		 2nd Digit	0 3rd Digit
CH2 [ 00 CH3 [ 00	1st Digit	2nd Digit	0 3rd Digit
	1st Digit	2nd Digit 7 2nd Digit	
	1st Digit	7	

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